

# **REPORT**

## **One Day Workshop on CAD for VLSI**

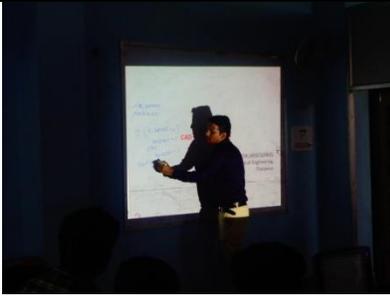
**CO ordinators**

**Mr.W.Newton David Raj AP/ECE**

**Mr.P.Raja Pirian AP/ECE**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
KINGS COLLEGE OF ENGINEERING PUNALKULAM**

One day work shop on CAD for VLSI organized by department of Electronics and communication engineering, to motivate the students to involve them in VLSI Domain and also the students got more ideas about tools used in CAD. **Dr.M.Joseph, Principal. St.Joseph College of Engineering and Technology, Thanjavur & Mr.T.Pasupathi AP/ECE , Kings college of Engineering** were the resource persons.

		
<p>Presidential Address By Mrs.T.Shanthi</p>	<p>Honoring the Chief Guest</p>	<p>Participants</p>
		
<p>Chief guest Address by Dr.M.Joseph</p>	<p>Hands on session by Mr.T.Pasupathi</p>	<p>Hands on session at VLSI Lab</p>

The workshop was begins at 10.10a.m, Mr.P.Raja Pirian AP/ECE welcomes the resource person and the gathering, followed by Mr.W.Newton David Raj AP/ECE introduced the chief guest, Mrs.T.Shanthi, Project officer delivered the inaugural address after that the chief guest was honored by Mrs.T.Shanthi. Finally Dr.M.Joseph delivered the chief guest address. Immediately the 1<sup>st</sup> session begins with a Lecture on Introduction to CAD for VLSI by Dr.M.Joseph. After the Lunch break The 2<sup>nd</sup> session was continued by Mr.T.Pasupathi AP/ECE at VLSI lab with CAD tools. The students were actively involved in the entire sessions. The program ends with valedictory.

Nearly 41 students were participated in the workshop; 28 students from our college and the remaining participants from ksk Engineering college and vandaiyar engineering college.

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